

Fig. 1

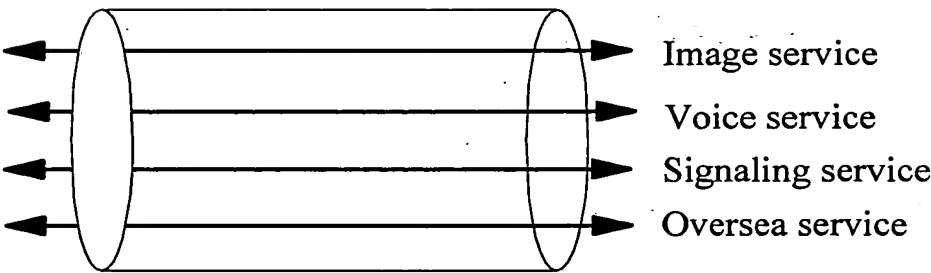


Fig. 2

VC4#1

VC4#2

VC4#3

(1,2)

(2,3)

(3,1)

time-division cross connecting

(3,1)

(1,2)

(2,3)

Figure 1 consists of three parts: (a), (b), and (c). Part (a) is a schematic diagram of a ring structure. It shows a ring with four nodes labeled A, B, C, and D. The ring is divided into two paths: an upper path labeled 'e' and a lower path labeled 'w'. Each path has a square component at each node. A label '401' is placed near the lower path. Part (b) shows three components labeled A, B, and C. Component A is a square with a shaded lower half. Component B is a square with a shaded lower half. Component C is a square with a shaded lower half. Each component has eight input/output ports labeled 1 through 8. Part (c) shows the three components (A, B, C) connected in a ring configuration, similar to the ring structure in (a). The components are labeled A, B, and C, and the paths are labeled 'e' and 'w'.

Fig. 4

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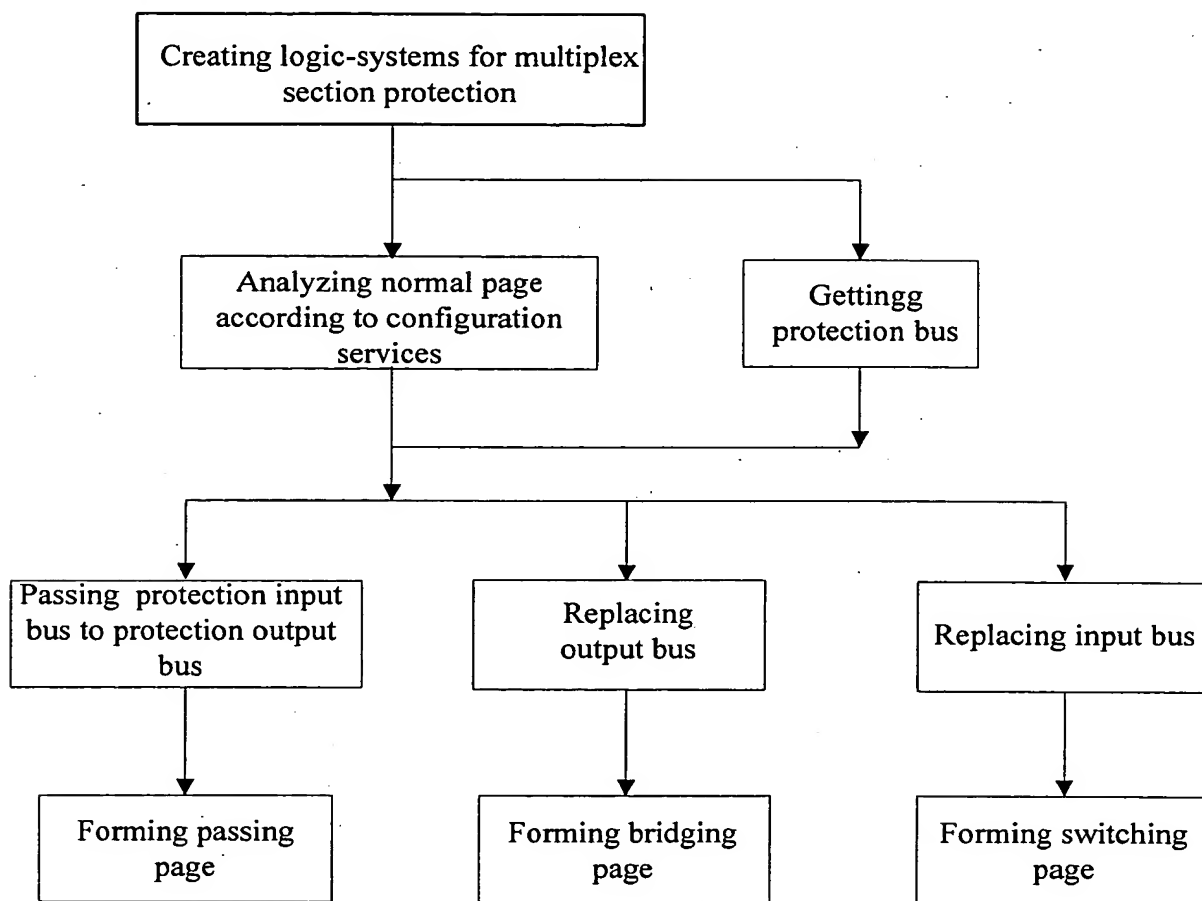


Fig. 5

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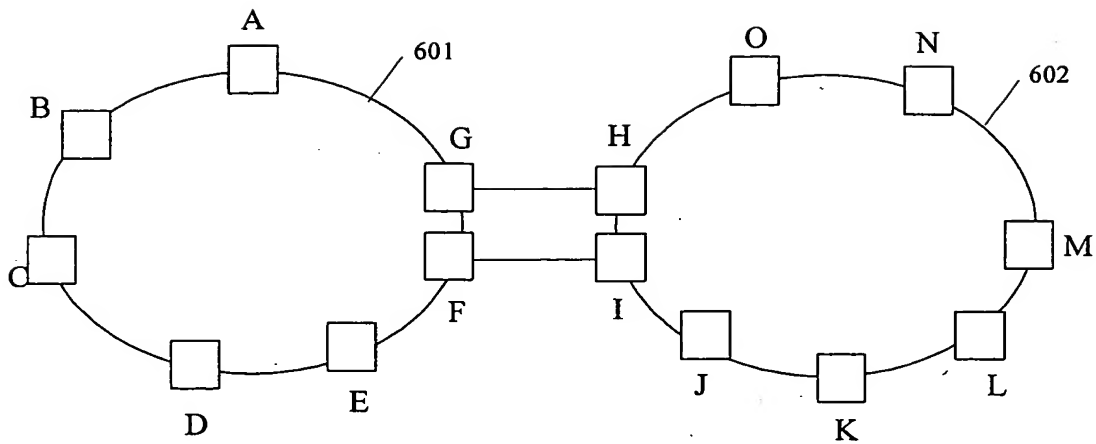


Fig. 6

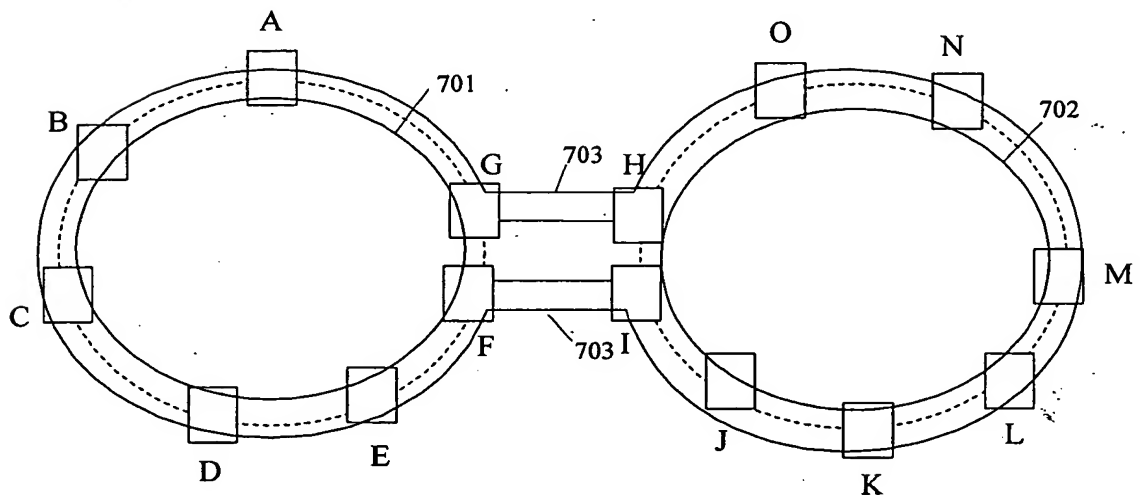


Fig. 7

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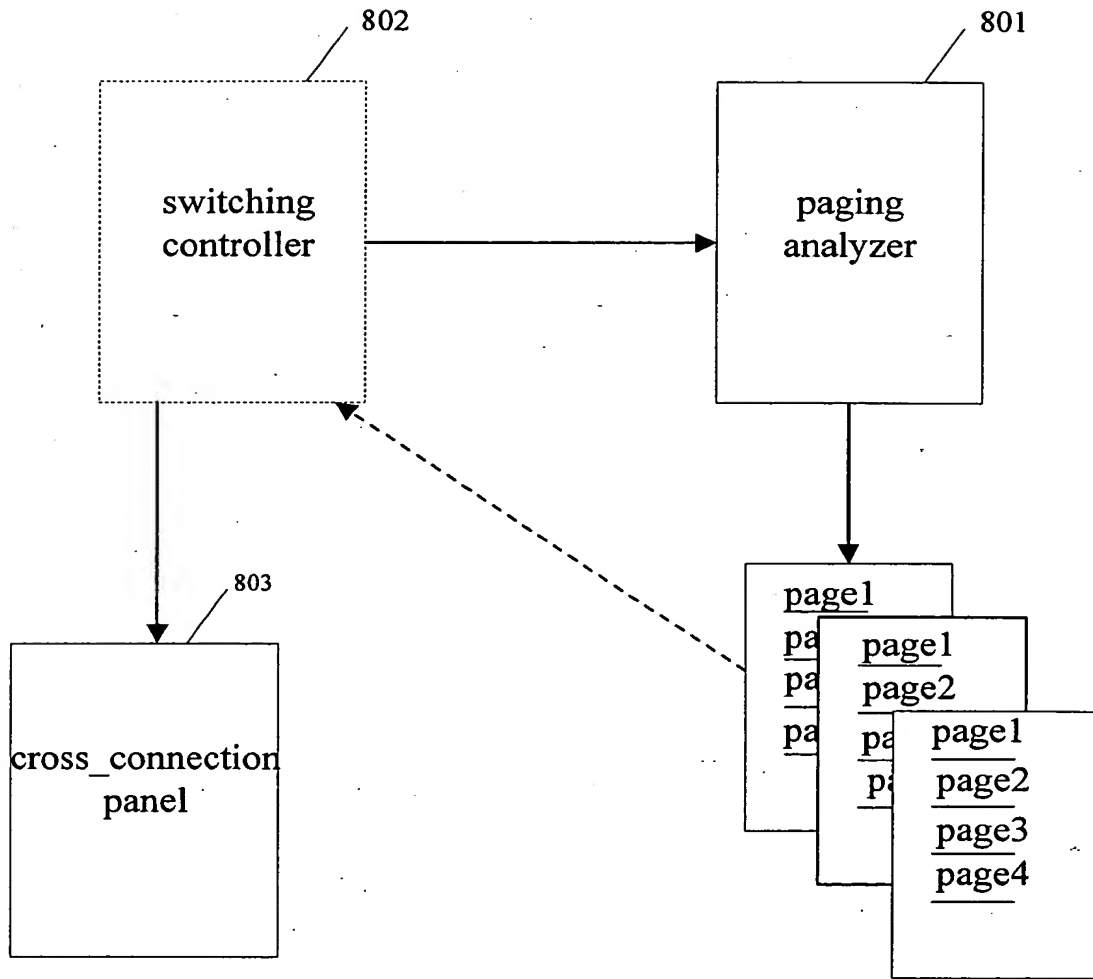


Fig. 8